

Technical  
Specifications

# PXIe Vector Network Analyzer VNA1009M

30 MHz to 9 GHz



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## Definitions

Specifications describe the performance of a model under stated operating conditions and are covered by the model warranty.

Typical (typ) describes additional product performance information that is not covered by the product warranty. It is performance beyond specifications that 80 percent of the units exhibit with a 90 percent confidence level at room temperature (approximately 25 °C). Typical performance does not include measurement uncertainty.

Nominal (nom) values indicate the expected mean or average performance, or an attribute whose performance is by design. This data is not warranted and is measured at room temperature (approximately 25 °C).

Measured (meas) describes an attribute measured during the design phase for purposes of communicating expected performance. This data is not warranted and is measured at room temperature (approximately 25 °C).

## Software Application

<b>Measurement Capabilities</b>	
Measured parameters	S11, S21, S22, S12
Number of measurement channels	256
Data traces	16 /channel
Measured points	2-4096 pts/trace
Data display formats	Logarithmic amplitude, linear amplitude, phase,
<b>Sweep Features</b>	
Linear frequency sweep	Support (minimum resolution 1kHz)
Frequency scan segmentation	Support (up to 256 segments/channel)
Power sweep	Support (1 dB minimum step)
<b>Trigger modes</b>	
Continuous trigger	Support
Single trigger	Support (Manual and external)
<b>Trace functions</b>	
Averaging	Support (up to 100 times)
IF bandwidth	1000kHz/300kHz/100kHz/30kHz/10kHz/3kHz/1kHz/300Hz/100Hz/30Hz/10Hz
Scale	Support
<b>Marker functions</b>	
Marker numbers	8 /trace
Reference marker	Support
<b>Calibration functions</b>	
Mechanical calibration	Support
Electronic calibration	Support for ancillary products (no third-party electronic calibrators)
Calibrated algorithm	Single-port (OSL), two-port (TR, ER, SOLT, TRL, UT)
Impedance conversion	Support
Port extension	Support
Embedding	Support
De-Embedding	Support
<b>System settings</b>	
Status storage	Support
Status restored	Support
Data storage	Support (sNp format)
Restore the presets	Support

## System Specifications

<b>Frequency range</b>	
<b>Model number</b>	VNA1009-M
Frequency range	30MHz~9GHz
Resolution	1kHz
Frequency accuracy	± 3ppm
<b>Frequency switching speed</b>	
Lockout time	15us
<b>Dynamic Range</b>	
30MHz~0.5GHz	96 dB
0.5GHz~6GHz	102 dB
6GHz~9GHz	96 dB

## Corrected performance (Use mechanical calibration Module)

<b>Directivity</b>	
30MHz~0.5GHz	40 dB
0.5GHz~6GHz	38 dB
6GHz~9GHz	36 dB
<b>Source Match</b>	
30MHz~0.5GHz	39 dB
0.5GHz~6GHz	34 dB
6GHz~9GHz	33 dB
<b>Load Match</b>	
30MHz~0.5GHz	40 dB
0.5GHz~6GHz	38 dB
6GHz~9GHz	36 dB
<b>Transmission Tracking</b>	
30MHz~0.5GHz	±0.08 dB
0.5GHz~6GHz	±0.12 dB
6GHz~9GHz	±0.18 dB
<b>Reflection Tracking</b>	
30MHz~0.5GHz	±0.07 dB
0.5GHz~6GHz	±0.10 dB
6GHz~9GHz	±0.15 dB

## Test Port Output

<b>Maximum output port power</b>	
30MHz~0.5GHz	+10 dBm
0.5GHz~6GHz	+10 dBm
6GHz~9GHz	+10 dBm
<b>Nominal power</b>	
30MHz~9GHz	0 dBm
<b>Power range</b>	
30MHz~0.5GHz	+10 dBm~ -30 dBm
0.5GHz~6GHz	+10 dBm~ -30 dBm
6GHz~9GHz	+10 dBm~ -30 dBm
<b>Power level accuracy</b>	
30MHz~0.5GHz	$\pm 1.0$ dB
0.5GHz~6GHz	$\pm 1.0$ dB
6GHz~9GHz	$\pm 1.0$ dB
<b>Source harmonics</b>	
30MHz~0.5GHz	-6 dBc
0.5GHz~6.5GHz	-8 dBc
6.5GHz~9GHz	-20 dBc

## Test Port Input

<b>Test port input damage level</b>	
30MHz~9GHz	>+27 dBm, > $\pm 35$ VDC, >1000V ESD
<b>Receiver level accuracy</b>	
30MHz~9GHz	$\pm 1$ dB
<b>Noise floor</b>	
30MHz~0.5GHz	-90 dBm
0.5GHz~6GHz	-96 dBm
6GHz~9GHz	-90 dBm
<b>Magnitude trace noise</b>	
30MHz~0.5GHz	0.006 dB rms
0.5GHz~6GHz	0.006 dB rms
6GHz~9GHz	0.006 dB rms
<b>Phase trace noise</b>	
30MHz~0.5GHz	0.04° rms
0.5GHz~6GHz	0.04° rms
6GHz~9GHz	0.04° rms

## Measurement speed

<b>Typical cycle time (full frequency span, 1000 kHz IF bandwidth, includes data transfer)</b>			
Number of points	201	401	801
Uncorrected	11 ms	16.7 ms	29.1 ms
2-port calibration	18.7 ms	29.9 ms	53.8 ms
<b>Typical cycle time(full frequency span, 100 kHz IF bandwidth, includes data transfer)</b>			
Number of points	201	401	801
Uncorrected	11.7 ms	19.9 ms	35.3ms
2-port calibration	20.7 ms	35.5ms	64.88 ms

## General Specifications

<b>Remote programming</b>	
Interfaces	PXIe (for PXI hybrid slots)
Control languages	Factory defined SCPI
<b>Power requirements</b>	
12VDC, 20W maximum	
<b>Operating temperature range</b>	
0 to 50°C	
<b>Storage temperature range</b>	
-20 to 70°C	
<b>Operating and storage altitude</b>	
Up to 15,000 feet	
<b>Humidity</b>	
Relative humidity type test:95%, +40°C (non-condensing)	
<b>Memorizer</b>	
Depends on the PC	
<b>Weight</b>	
≤0.5kg	
<b>Size</b>	
Single-slot PXI:	
Height: 128.4mm ± 1mm	
Width: 19.9mm ± 1mm	
Length: 212.6mm ± 1mm	
<b>Recommended calibration cycle</b>	
24 months	
<b>ISO compliant</b>	
This instrument is manufactured in an ISO-9001 registered facility in concurrence with RF-Cube commitment to quality.	

## Connectors

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<b>Front panel connectors</b>	
Test Port (Port 1/2)	RF signals are input/output via SMA female connectors Impedance: 50 $\Omega$ (nominal) Input damage level: +27dBm
Reference input	Connector: MMPX Accept a 10 MHz reference signal used to frequency lock the internal time base; nominal input level 0 dBm to 10 dBm, impedance 50 $\Omega$ , sine wave.
10MHz output	Connector: MMPX Output the 10 MHz reference signal used by internal time base. level nominally +3 dBm; nominal output impedance 50 $\Omega$ ; input damage level is +18 dBm.
Local oscillator input/output	Connector: SMA female Impedance: 50 $\Omega$ (nominal) Output nominal level: -2dBm Input damage level: +20dBm
Trigger in	Connector: MMPX Trigger Type: Edge Impedance: 1k $\Omega$ (nominal) Level Range: 3.3V CMOS (TTL Compatible, 5V Tolerant)
Trigger out	Connector: MMPX Level Range: 3.3V CMOS (TTL Compatible, 5V Tolerant)
The trigger output is ready	Connector: MMPX Impedance: 50 $\Omega$ (nominal) Level Range: 3.3V CMOS (TTL Compatible, 5V Tolerant)

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